CLAIMS

33. (currently amended) Signal processing apparatus comprising a signal amplifier and a frequency converter which operate in succession on an input signal, and a lineariser which is provided between the amplifier and the frequency converter to introduce a correction signal that is adapted to make the overall input and output characteristic of the apparatus more linear by linearising both the amplifier and frequency converter [[means]].

34. (previously presented) Signal processing apparatus according to Claim 33, wherein a feedback signal derived from the output of the apparatus is used by the lineariser to adapt the correction signal.

35. (previously presented) Signal processing apparatus according to Claim 34, further comprising a pilot signal generator for introducing a pilot signal into the input signal prior to frequency conversion and amplification, wherein the feedback signal contains distortion components from the pilot signal produced by at least one of the frequency converter and the amplifier.

36. (previously presented) A signal processing apparatus according to Claim 35, wherein the pilot signal is one of a CW carrier signal, a full carrier AM signal, a suppressed carrier AM signal, a single sideband signal, a quadrature amplitude modulated signal, a filtered quadrature phase shift keyed signal, a direct sequence spread spectrum signal, and a frequency hopped carrier signal modulated with any of the foregoing kinds of signal.

37. (previously presented) Signal processing apparatus according to Claim 35, wherein the pilot signal is one of a two-tone pilot signal and a multi-tone pilot signal.

38. (previously presented) A signal processing apparatus according to Claim 35, wherein the pilot signal is removed from the output of the apparatus by a filter or by the introduction of a pilot cancellation signal.

39. (previously presented) Signal processing apparatus according to Claim 38, wherein the pilot cancellation signal is adjusted using feedback derived from the output of the apparatus.

40. (previously presented) Signal processing apparatus according to Claim 38, wherein the pilot cancellation signal comprises a frequency converted, phase shifted and amplitude adjusted version of the pilot signal.

41. (previously presented) Signal processing apparatus according to Claim 38, wherein a digital signal processor is used to control the pilot cancellation signal using feedback from the output of the signal processing apparatus.

42. (currently amended) Signal processing apparatus according to Claim 38, further comprising a suppressor for cancelling signals which are images of the pilot signal.

43. (previously presented) Signal processing apparatus according to Claim 33, wherein a digital signal processor is used to control the correction signal using feedback from the output of the signal processing apparatus.

44. (previously presented) Signal processing apparatus according to Claim 33, wherein the lineariser comprises a distortion generator for producing the correction signal from the output signal of whichever of the amplifier and the frequency converter precedes it,

45. (previously presented) Signal processing apparatus according to Claim 44, wherein the distortion generator comprises a non-linearity generator.

46. (previously presented) Signal processing apparatus according to Claim 45, wherein the non linearity generator uses at least one of anti-parallel diodes, a FET channel, dual gate GaAsFETs operating close to pinch-off, Shottky diodes, mixers and multipliers in the non-linearity generating process.

47. (previously presented) Signal processing apparatus according to Claim 46, wherein the non linearity generator is arranged to generate the non-linearity by mixing its input signal with itself one or more times to produce the non-linearity.

48. (previously presented) Signal processing apparatus according to Claim 47, wherein the non linearity generator is arranged to generate a third order non-linearity by mixing the input to the non-linearity generator with itself and then with its input.

49. (previously presented) Signal processing apparatus according to Claim 47, wherein components of the non-linearity are generated and controlled separately.

50. (previously presented) Signal processing apparatus according to Claim 49, wherein in-phase and quadrature signals are produced from each separately generated non-linearity component and are controlled separately.

51. (previously presented) Signal processing apparatus according to Claim 33, wherein the frequency converter comprises a mixer for mixing a mixing signal into a received signal destined to be frequency converted.

52. (previously presented) Signal processing apparatus according to Claim 33, wherein the frequency converter is an upconverter for converting an intermediate frequency band signal into a radio frequency band signal.

53. (previously presented) Signal processing apparatus according to Claim 52, wherein the frequency converter comprises in-phase and quadrature signal paths for handling in-phase and quadrature signals representing a signal at the intermediate frequency band, wherein there is a separate, independently controlled, lineariser operating on each of these signal paths.

54. (previously presented) Signal processing apparatus according to Claim 33, wherein the frequency converter is a downconverter for converting a radio frequency band signal into an intermediate frequency band signal.

55. (previously presented) Signal processing apparatus according to Claim 54, wherein the frequency converter comprises in-phase and quadrature signal paths for handling in-phase and quadrature signals representing a signal at the intermediate frequency band, wherein there is a separate, independently controlled, lineariser operating on each of these signal paths.

56. (previously presented) Signal processing apparatus according to Claim 33, wherein the input signal is a CDMA signal.

57. (previously presented) A method of processing an input signal to produce an output signal, the method comprising the steps of signal amplification and frequency conversion, and the step of introducing, between the steps of amplification and frequency conversion, a correction signal that is adapted to make the overall input and output characteristic of the signal processing method more linear by linearising both the amplification and frequency conversion.

58. (previously presented) A method according to Claim 57, comprising the step of using a feedback signal derived from the output signal of the signal processing method to adapt the correction signal.

59. (previously presented) A method according to Claim 58, further comprising the step of introducing a pilot signal into the input signal prior to frequency conversion and amplification, wherein the feedback signal contains distortion components from the pilot signal produced by at least one of the frequency conversion and amplification steps.

60. (previously presented) A method according to Claim 59, further comprising the step of removing the pilot signal from the output signal of the method by filtering or by introducing a pilot cancellation signal.

61. (previously presented) A method according to Claim 60, comprising the step of adjusting the pilot cancellation signal using feedback derived from the output signal of the signal processing method.

62. (previously presented) A method according to Claim 57, wherein the correction signal is produced by a step of distorting the signal produced by whichever of the amplifying and frequency conversion steps precedes it.

63. (previously presented) A method according to Claim 62, wherein the step of distortion generation comprises the step of generating and controlling non-linearity components independently.

64. (previously presented) A method according to Claim 57, wherein the input signal is a CDMA signal.